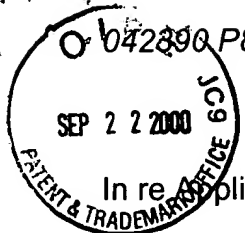


RECEIPT

Patent



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Brian K. Holscher

Serial No. 09/607,783

Filed: June 30, 2000

For: BUFFER ALLOCATION CIRCUIT

Assistant Commissioner for Patents
Washington, D.C. 20231

Examiner:

Art Unit:

REQUEST TO CORRECT AN ERROR IN THE FILING RECEIPT

Dear Sir:

Applicant respectfully notes that the title of the invention on the Filing Receipt for the above-identified application is incorrect. That Filing Receipt erroneously lists the title of the invention as MINIMUM GATE COUNT BUFFER ALLOCATION CIRCUIT. The title of the invention is in fact BUFFER ALLOCATION CIRCUIT.

Therefore, correction with respect to the inventors listed in the Filing Receipt of the present application is respectfully requested. Please change the title of invention of the present application to BUFFER ALLOCTAION CIRCUIT.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: Sept 14, 2000

R. Alan Burnett
R. Alan Burnett
Reg. No. 46,149

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on September 14, 2000
Date of Deposit
Dominique Valentino
Name of Person Mailing Correspondence
Dominique Valentino 9-14-00
Signature Date



Attorney Docket: 042390.P8836

APPLICATION FOR UNITED STATES LETTERS OF PATENT

FOR

BUFFER ALLOCATION CIRCUIT

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Inventor(s): **BRIAN K. HOLSCHER**

Prepared by:

BLAKELY SOKOLOFF TAYLOR & ZAFMAN, LLP
12400 Wilshire Boulevard, 7th Floor
Los Angeles, California 90025
(425) 827-8600

"Express Mail" Label Number EL 431686995US

Date of Deposit June 30, 2000

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Dominique Valentino

Date



incorrect

MINIMUM GATE COUNT BUFFER ALLOCATION CIRCUIT

BACKGROUND OF THE INVENTION

5 1. Field of the Invention

The present invention generally concerns buffer allocation in processing devices, and in particular concerns allocation of a next available buffer in a microprocessor.

2. Background Information

10 A common approach in trying to find the next available buffer in a processing device is to implement priority encoders, which determine the next available buffer through use of a pointer to the current buffer and a bit vector representing the combination of available buffers. For each pointer position, a priority encoder determines the next buffer from the available buffer bit vector. For example, if 8
15 buffers were to be implemented in this manner, 8 priority encoders would typically be used. The output of all the encoders is then driven to a multiplexer (mux), which uses the pointer to the current buffer to pick which encoder's output to use. Each encoder is for a different pointer value, and accordingly, cannot be shared by other pointer values.

20 The following truth tables correspond to an exemplary 3-buffer configuration that employs priority encoders.

Encoder 0:		
	Input	Output
25	000	000
	001	001
	010	010

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BUFFER ALLOCATION CIRCUIT

Correct

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Encoder 0:

25	Input	Output
	000	000
	001	001
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DISCREPANCY FORM

Please note that the attached matter was received from the PTO with the below-listed discrepancy:

Matter No.: P8839

Date: 9/13/2000

To Attorney: RAB

From: JERRY

Filing Date incorrect:

Our Records State:

Serial Number incorrect:

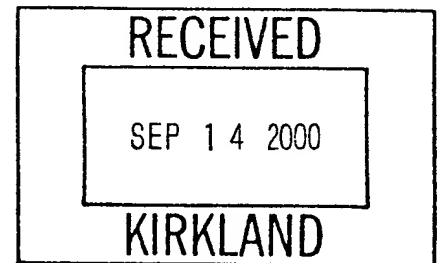
Our Records State:

Inventor's name spelled incorrectly:

Listed as:

Our Records State:

Inventor name not included:



Title not Correct: X

Listed as: MINIMUM GATE COUNT BUFFER ALLOCATION CIRCUIT

Our Records State: BUFFER ALLOCATION CIRCUIT

Notes:

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/607,783	06/30/2000	2762	690	042390.P8839	3	16	3

EAT

R Alan Burnett
Blakely Sokoloff Taylor & Zafman LLP
12400 Wilshire Boulevard 7th Floor
Los Angeles, CA 90025RECEIVED
AUG 31 2000BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN
LOS ANGELES

Date Mailed: 08/18/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Brian K. Holscher, Hillsboro, OR ;

Continuing Data as Claimed by Applicant

Foreign Applications

If Required, Foreign Filing License Granted 08/18/2000

Title

Minimum gate count buffer allocation circuit

Preliminary Class

706

Data entry by : ASGEDOM, GENET

Team : OIPE

Date: 08/18/2000



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- The articles such as "a," "an" and "the" are not included as the first words in the title of an application. They are considered to be unnecessary to the understanding of the title.
- The words "new," "improved," "improvements in" or "relating to" are not included as first words in the title of an application because a patent application, by nature, is a new idea or improvement.
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- The docket number allows a maximum of 25 characters.
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Bib Data Sheet

SERIAL NUMBER 09/607,783	FILING DATE 06/30/2000 RULE -	CLASS 706	GROUP ART UNIT 2762	ATTORNEY DOCKET NO. 042390.P8839	
APPLICANTS Brian K. Holscher, Hillsboro, OR ; ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS ***** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 08/18/2000					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY OR	SHEETS DRAWING 3	TOTAL CLAIMS 16	INDEPENDENT CLAIMS 3
ADDRESS R Alan Burnett Blakely Sokoloff Taylor & Zafman LLP 12400 Wilshire Boulevard 7th Floor Los Angeles ,CA 90025					
TITLE Buffer allocation circuit					
FILING FEE RECEIVED 690	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		